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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/786,677	02/25/2004	Stewart S. Taylor	884.B91US1	3000
21186	7590	07/10/2006		EXAMINER
				MAI, LAM T
			ART UNIT	PAPER NUMBER
			2819	

DATE MAILED: 07/10/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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<b>Office Action Summary</b>	Application No.	Applicant(s)
	10/786,677	TAYLOR ET AL.
	Examiner	Art Unit
	LAM T. MAI	2819

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) Responsive to communication(s) filed on 15 June 2006.
- 2a) This action is FINAL.                            2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) 26-28 is/are allowed.
- 6) Claim(s) 1-5,7-14 and 16-25 is/are rejected.
- 7) Claim(s) 6 and 15 is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All    b) Some \* c) None of:
  1. Certified copies of the priority documents have been received.
  2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other: \_\_\_\_\_.

**DETAILED ACTION**

**Response to Amendment**

Applicant's argument filed one 6/15/2006 has been carefully considered. The argument indicates that the Dolman (USP 6,396,345) reference does not teach the invention as claimed. The examiner was not convinced. The examiner assert that figure 1 of Dolman does discloses all limitation of claim 1 as applicant argued. As claimed in claim 1, Dolman does teach an amplifier (122), to produce an output (128) and to receive an input (119) including an phase adjustable (118) to adjust phase in response to an output having indication of amplitude (124 and 150) to reduce a phase error (distortion). The amplitude (124, 150) of the amplifier output feedback to again (amplitude) detector the from the gain detector to predistorner to adjust or control phase error and gain correction (119). Dolman has taught more detail than as claimed in claim 1. Dolman does read on the claimed invention. Therefore, the examiner maintains position that the office action dated 4/19/2006 is in effect.

***Specification***

The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5,7,16-24 are rejected under 35 U.S.C. 102(b) as being anticipated by Dolman (USP 6,396,345).

Regarding claim 1, Dolman discloses a apparatus and method that teaches an amplifier (122) to produce an output signal (128) and receive an input signal (119) including an adjustable phase (118) to be adjusted in response to an indication of an amplitude (126) of the output (128) signal to reduce a phase distortion (see figures 1 and 3 and their descriptions).

Regarding claim 2, Dolman teaches an envelope detector (160,152, 126) to detect the indication of the amplitude.

Regarding claim 3, Dolman teaches varactor wherein the adjustable phase is to be adjusted.

Regarding claim 4, Dolman teaches translation circuit (170) to transform the indication of amplitude into a control signal to adjust the adjustable phase.

Regarding claim 5, Dolman teaches translation circuit is to provide a loop gain of less than about one.

Regarding claim 7, Dolman teaches an amplifier (122) includes CMOS technology.

Regarding claim 16, Dolman discloses a method that teaches detecting an indication of an output signal of an amplifier (122) and adjusting a phase of an input signal in response to an indication of an amplitude (126) of the output (128) signal to

reduce a change in a phase distortion of the output signal (see figures 1 and 3 and their descriptions).

Regarding claim 17 and 18, Dolman teaches detecting an envelope of the amplitude and a peak value of the amplitude of the output signal.

Regarding claim 19, Dolman teaches the amplitude of the output signal includes an output signal power value.

Regarding claim 20, Dolman teaches reducing the change in the phase of the output signal.

Regarding claim 21, Dolman teaches reducing a change in the amplitude of the output signal.

Regarding claim 22, Dolman discloses a method that performing detecting an indication of an output signal of an amplifier (122) and adjusting a phase of an input signal in response to an indication of an amplitude (126) of the output (128) signal to reduce a change in a phase distortion of the output signal (see figures 1 and 3 and their descriptions).

Regarding claim 23, Dolman teaches controlling a variable turning element at the input of an amplification stage included in the amplifier.

Regarding claim 24, Dolman teach performance of the amplifier stage that including adjusting bias values of an amplification stage included in the amplifier to reduce amplitude distortion included in the output signal.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 8-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dolman as applied to claim 1 above, and further in view of Prasad et al (USP 6,515,540).

Regarding claim 8-10, Dolman discloses a apparatus and method that teaches an amplifier (122) to produce an output signal (128) and receive an input signal (119) including an adjustable phase (118) to be adjusted in response to an indication of an amplitude (126) of the output (128) signal to reduce a phase distortion (see figures 1 and 3 and their descriptions). Dolman fails to teach multiple stage amplifier system.

While Prasad discloses a multiple stage amplifier system including at least three stage amplifier system.

It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate Dolman's amplifier circuit into Prasad multiple stage amplifier system to improve and control amplitude and phase of the output of the amplifier for each of the stage.

Claims 11-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dolman as applied to claim 1 above, and further in view of Miyatani (USP 6,388,518).

Regarding claim 11, Dolman discloses a apparatus and method that teaches an amplifier (122) to produce an output signal (128) and receive an input signal (119) including an adjustable phase (118) to be adjusted in response to an indication of an amplitude (126) of the output (128) signal to reduce a phase distortion (see figures 1 and 3 and their descriptions). Dolman fails to teach antenna coupled to the amplifier.

While, Miyatani discloses a system with distortion compensation that includes an amplifier circuit and an antenna coupled to the amplifeir.

It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporation Dolman's amplifier circuit into Miyatani system to improve amplitude and phase error correction.

Regarding claim 12, Dolman teaches translation circuit to transform the indication of amplitude into a control signal.

Regarding claim 13, Dolman teaches tuning element to receive the control signal to adjust the adjustable phase.

Regarding claim 14, Dolman teaches adjustable phase is capable of being adjusted while leaving a signal amplitude associated with the amplifier substantially unchanged.

Claims 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dolman as applied to claim 22 above, and further in view of Prasad et al (USP 6,515,540).

Regarding claim 25, Dolman fails to teach or suggest second stage. While, Prasad discloses multiple stages amplifier which is included at least three stages.

I would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate Dolman's amplifier circuit into Prasad multiple stage amplifier system to improve control phase and amplitude of the amplification system.

***Claim Rejections - 35 USC § 101***

35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

Claims 22-25 are rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter. Claims 22-25 are an article per se not stored on a computer readable medium in executable form to enable it to cause an article to perform a practical application with useful, concrete and tangible result.

***Allowable Subject Matter***

Claims 26-28 are allowable. The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach or suggest a translinear circuit to be coupled to the second input and to the indication and to adjust the adjustable phase.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Claims 6 and 15 objected to as being dependent upon a rejected base claim, but they would be considered for allowable if they are rewritten in independent form including all of the limitations of the base claim and any intervening claims. The features of objected claims are not taught or suggested in the prior art.

### **Cited References**

The cited references relate to multiple stage amplifier system and phase and amplitude control of the amplifier for each stage of the system.

### ***Conclusion***

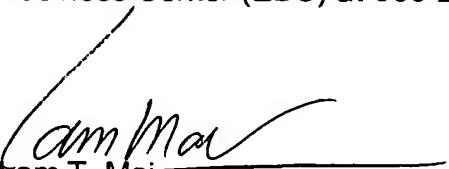
**THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to LAM T. MAI whose telephone number is (571)272-1807. The examiner can normally be reached on 5:30 am - 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Barnie Rexford can be reached on (571) 272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
\_\_\_\_\_  
Tam T. Mai

Art Unit 2819